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JUL 22 2004

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23494 7590 04/28/2004

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Attn: Jacqueline J. Garner

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Marianna Smith	(Depositor's name)
<i>Marianna Smith</i>	(Signature)
7-15-04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/602,994	06/24/2003	PR Chidambaram	TI-35608	1602

TITLE OF INVENTION: METHOD FOR PRODUCING LOW/HIGH VOLTAGE THRESHOLD TRANSISTORS IN SEMICONDUCTOR PROCESSING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	07/28/2004
EXAMINER	ART UNIT		CLASS-SUBCLASS		
LINDSAY JR, WALTER LEE	2812		438-289000		

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Texas Instruments Incorporated

Dallas, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent); individual corporation or other private group entity government

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(Authorized Signature) *Jacqueline J. Garner* (Date) 7/15/2004
Jacqueline J. Garner, Reg. No. 36,144

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